

WEST Search History

DATE: Wednesday, February 28, 2007

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=PGPB, USPT; PLUR=YES; OP=OR</i>			
<input type="checkbox"/>	L12	l9 and L11	51
<input type="checkbox"/>	L11	L10 and l4	161
<input type="checkbox"/>	L10	(antenna or diode) with (primary or input or port)	129168
<input type="checkbox"/>	L9	l4 and L8	109
<input type="checkbox"/>	L8	(antenna or diode) with (programmable or pld or clb or fpga or "standard cell" or "standard-cell" or block or core or embedded)	43076
<input type="checkbox"/>	L7	l6 and (antenna or diode or "top-level" or toplevel or "top level")	9
<input type="checkbox"/>	L6	l4 and L5	9
<input type="checkbox"/>	L5	(antenna or diode) with ("top-level" or toplevel or "top level")	78
<input type="checkbox"/>	L4	716/\$[ccls]	11662
<input type="checkbox"/>	L3	l1 and (programmable or pld or clb or fpga or standard or block or core or embedded)	1
<input type="checkbox"/>	L2	l1 and (primary or input or port or level or "top-level" or toplevel or "top level" or "i/o" or "i-o" or io)	1
<input type="checkbox"/>	L1	6594809[pn]	1

END OF SEARCH HISTORY